

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2579	FUSE WITH (INTEGRATED NEAR3 CIRCUIT)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/04/29 12:27
L2	4211	FUSE WITH (INTEGRATED NEAR3 CIRCUIT CIP DIE 'IC')	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/04/29 12:27
L3	696	(FUSE WITH (INTEGRATED NEAR3 CIRCUIT CIP DIE 'IC')) with laser	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/04/29 12:27
L4	54	(FUSE WITH (INTEGRATED NEAR3 CIRCUIT CIP DIE 'IC')) with laser near3 energy	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/04/29 12:28
L5	36	4 and substrate	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/04/29 12:30
L6	17	5 and ((surface side) near3 (top bottom first second front back))	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/04/29 12:35
L7	27	4 and ((surface side) near3 (top bottom first second front back))	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/04/29 12:39
L8	4	7 and laser with repair\$3	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/04/29 12:37
L9	551	(laser with fuse) with (repair\$3 activat\$3)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/04/29 12:38
L10	123	9 and laser near4 energy	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/04/29 12:38

L11	54	10 and ((surface side) near3 (top bottom first second front back))	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/04/29 12:39
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